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BIBDATASHEET**CONFIRMATION NO. 3295**

Bib Data Sheet

SERIAL NUMBER 10/797,484	FILING DATE 03/09/2004 RULE	CLASS 326	GROUP ART UNIT 2819	ATTORNEY DOCKET NO. 174/173CON3
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**** CONTINUING DATA *******

This application is a CON of 10/299,572 11/18/2002 PAT 6,727,727
which is a CON of 10/017,199 12/14/2001 PAT 6,525,564
which is a CON of 09/517,146 03/02/2000 PAT 6,366,120
which claims benefit of 60/122,788 03/04/1999
and claims benefit of 60/142,431 07/06/1999

*OK Lta***** FOREIGN APPLICATIONS ********None Lta***IF REQUIRED, FOREIGN FILING LICENSE GRANTED****** 05/26/2004**

Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY CA	SHEETS DRAWING 9	TOTAL CLAIMS 14	INDEPENDENT CLAIMS 1
Verified and Acknowledged <i>[Signature]</i>	Examiner's Signature <i>[Signature]</i> Initials <i>Lta</i>				

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TITLE

Interconnection resources for programmable logic integrated circuit devices